

Fig. 1

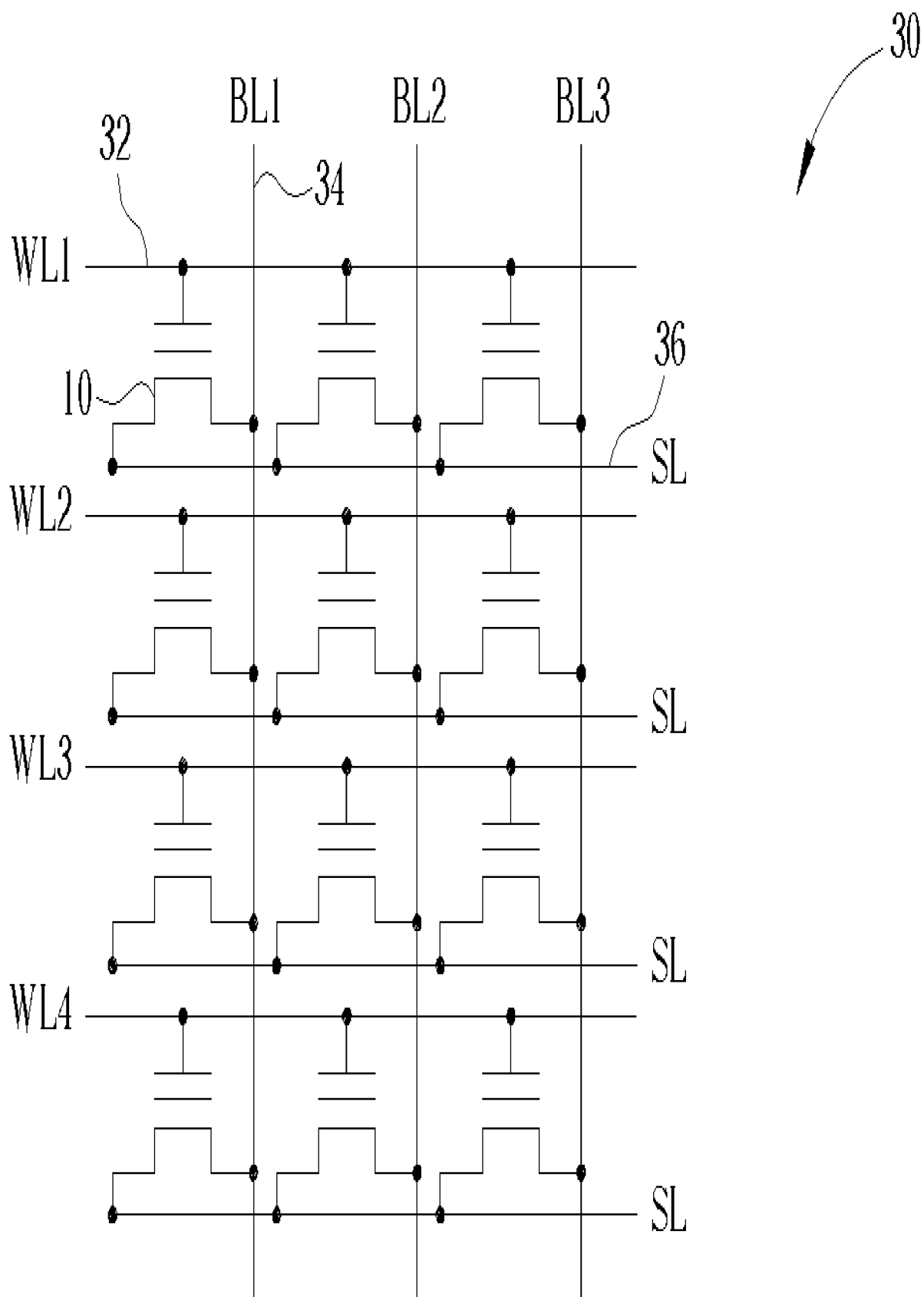


Fig. 2

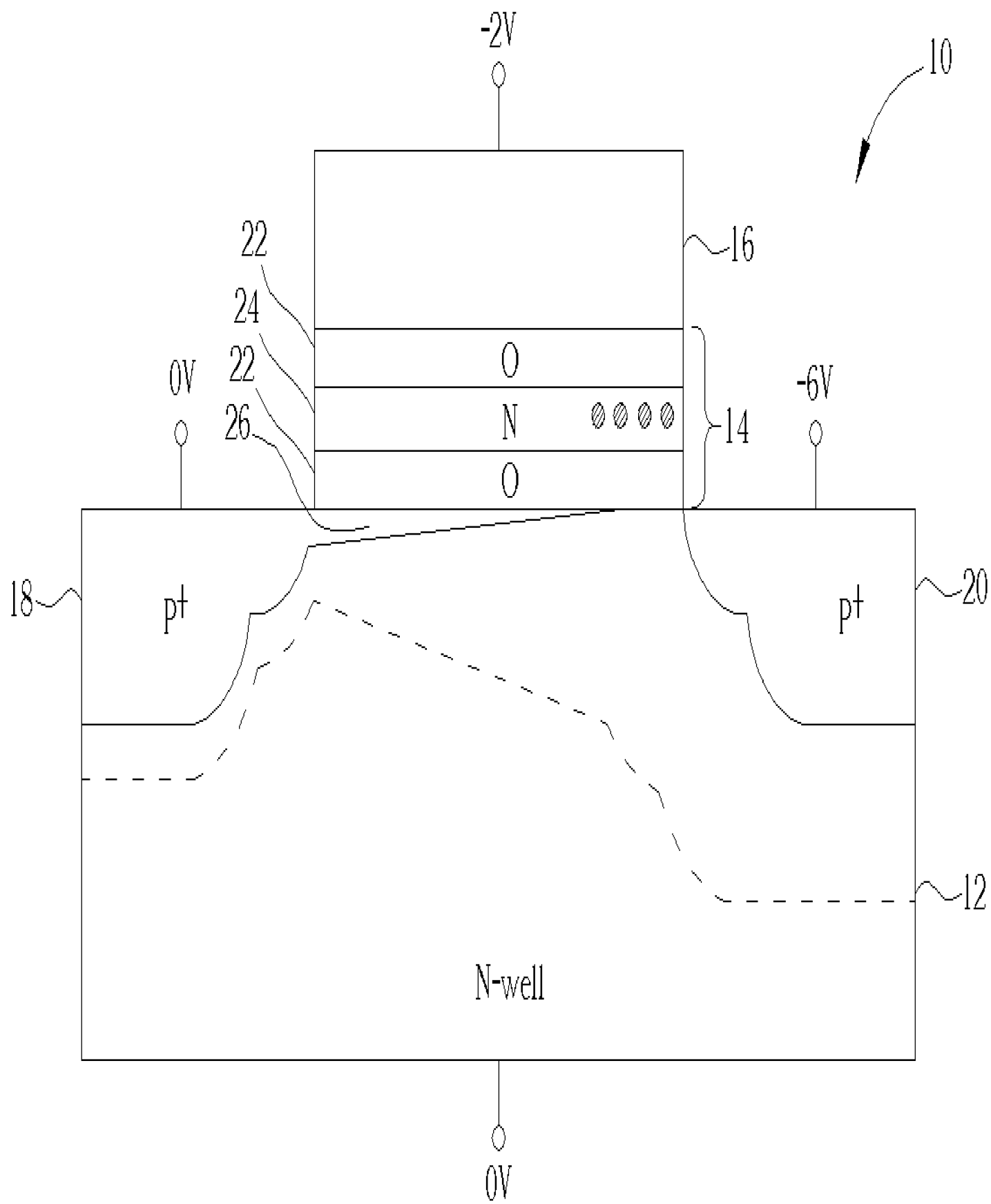


Fig. 3

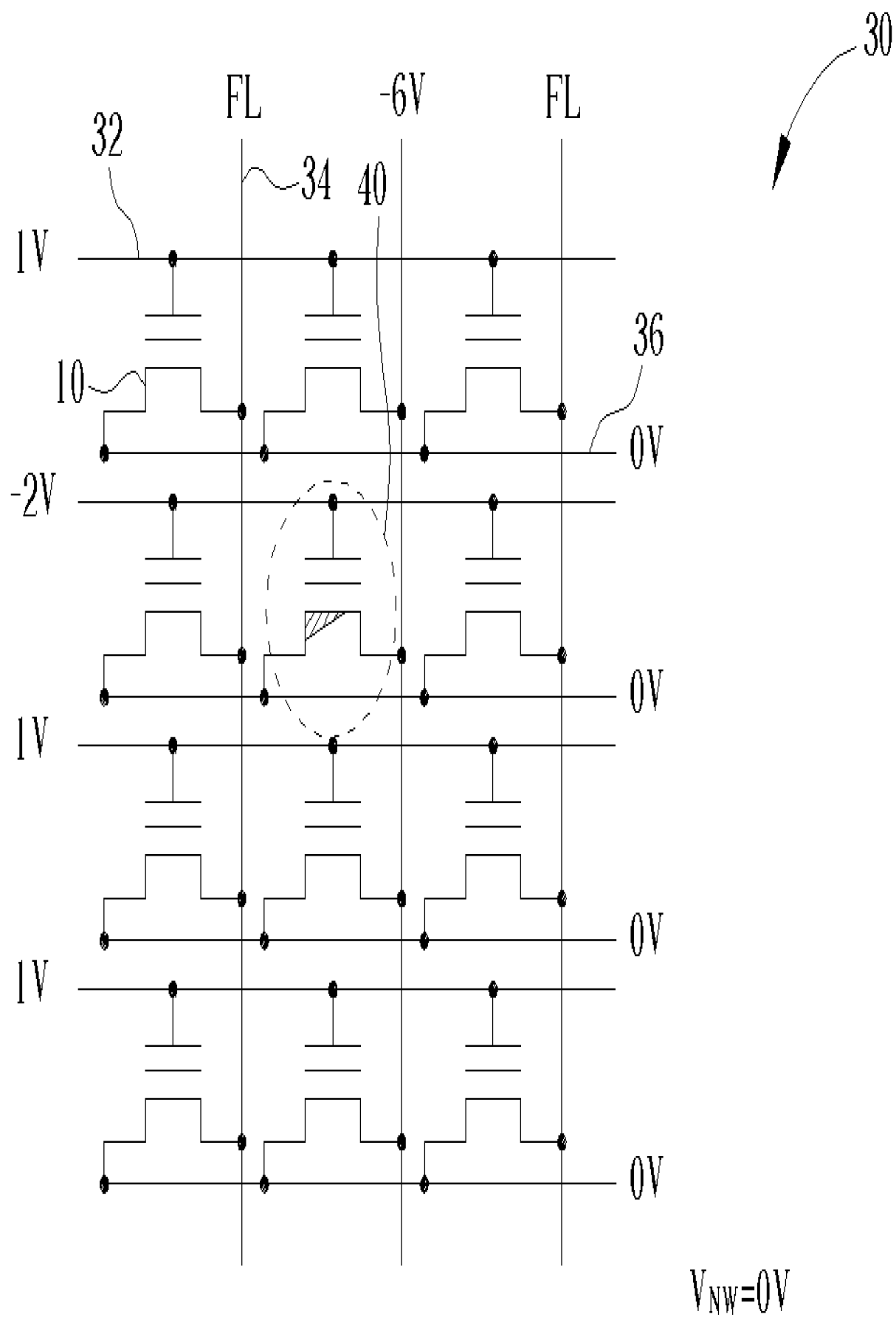


Fig. 4

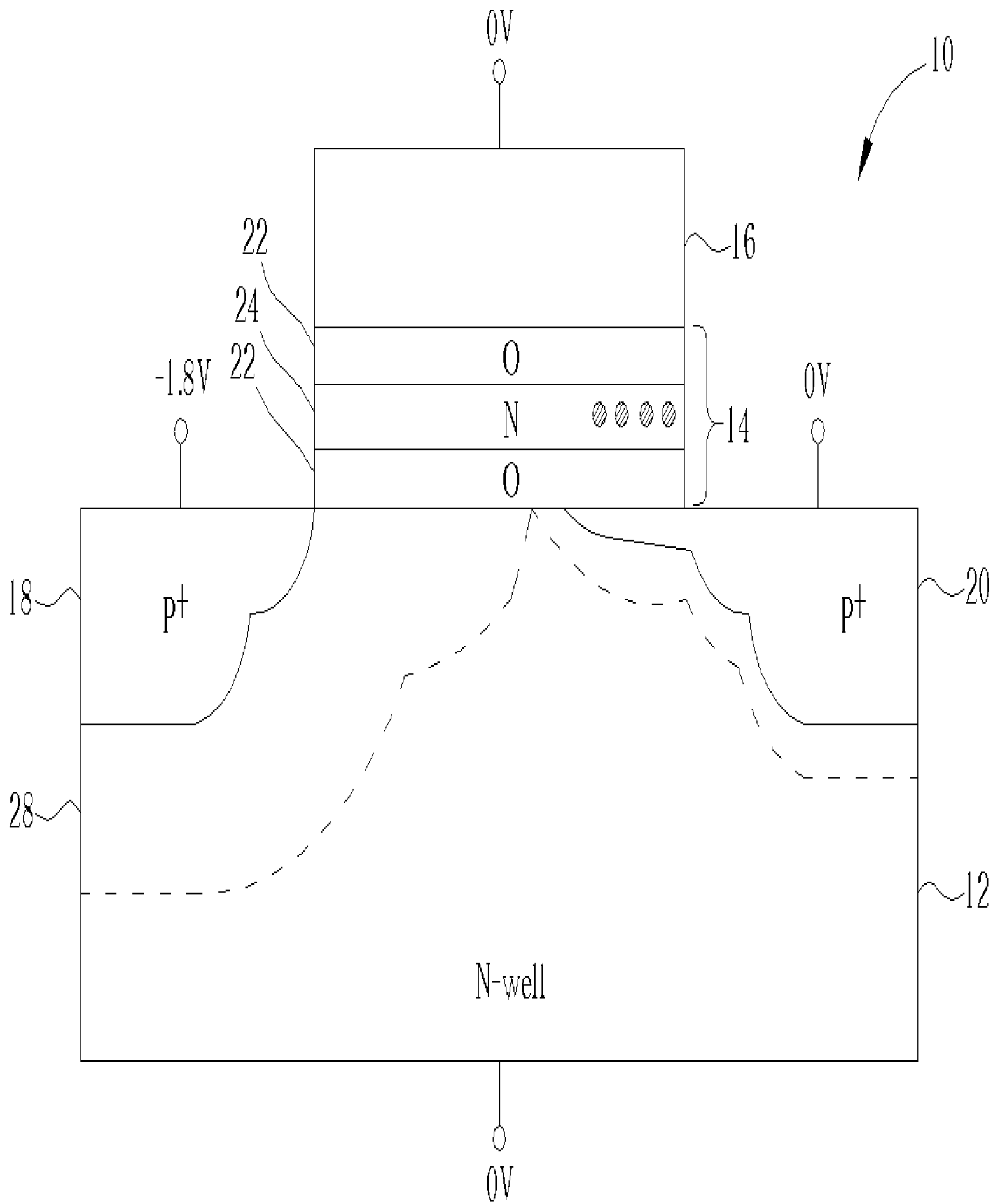


Fig. 5

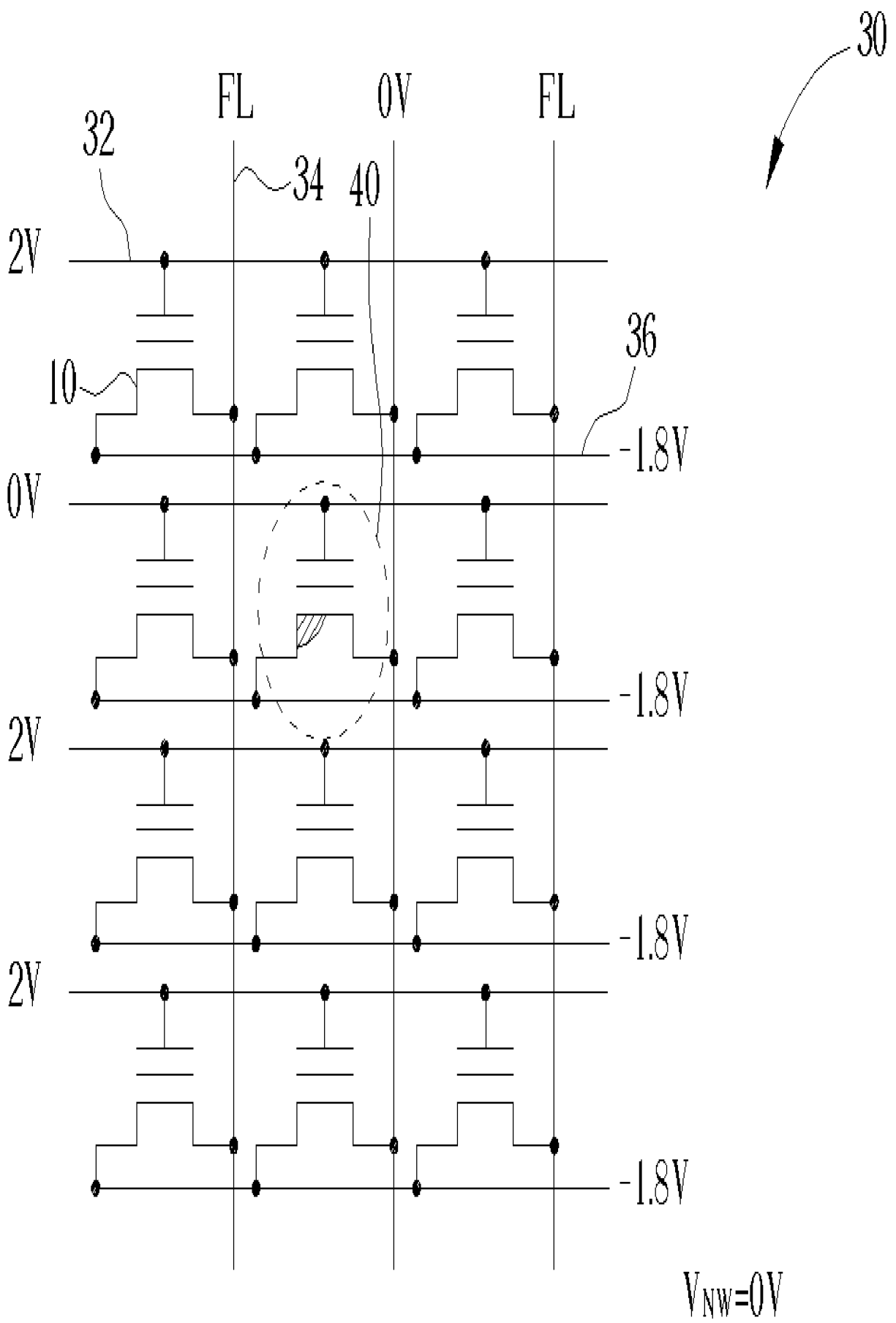


Fig. 6

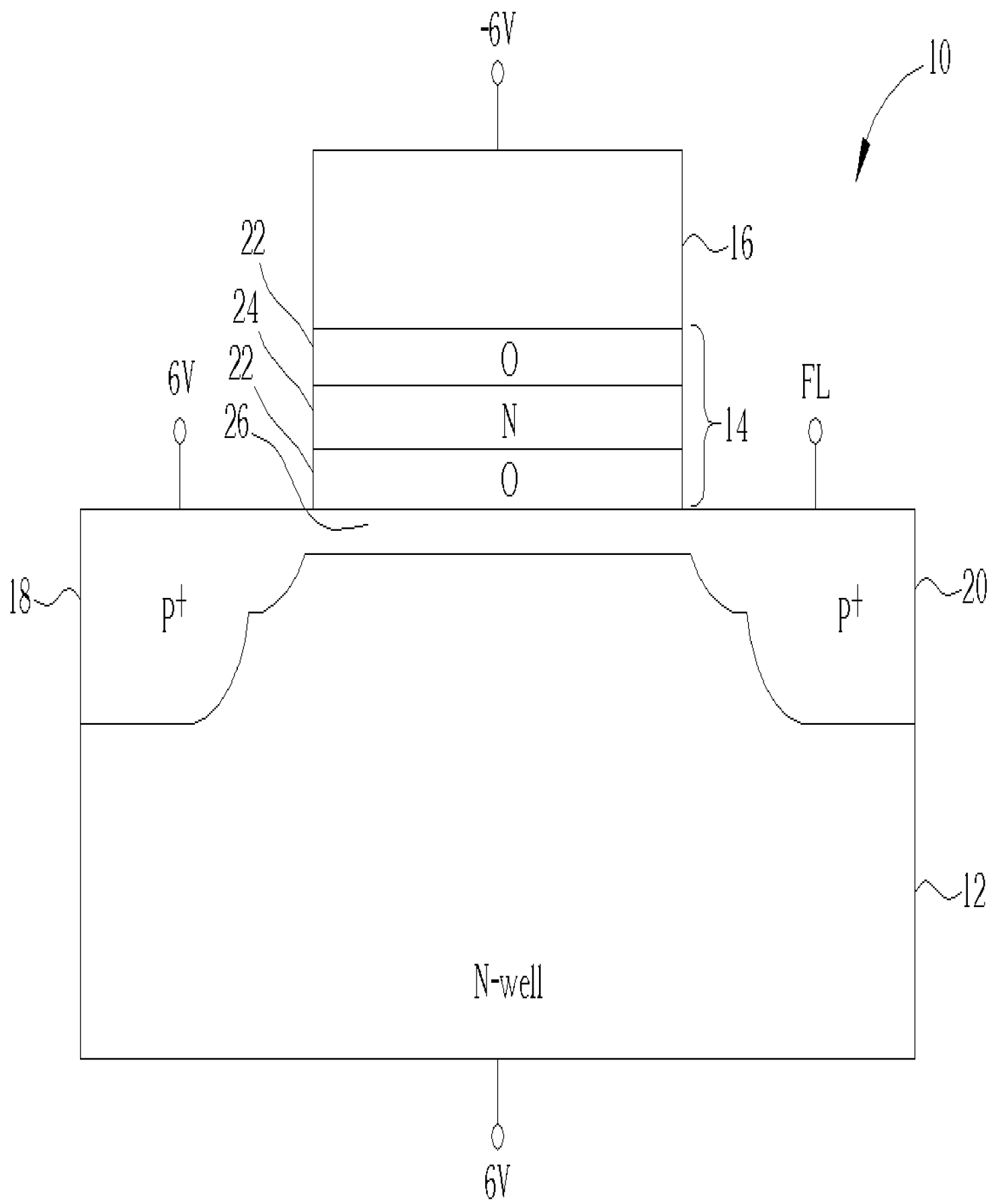


Fig. 7

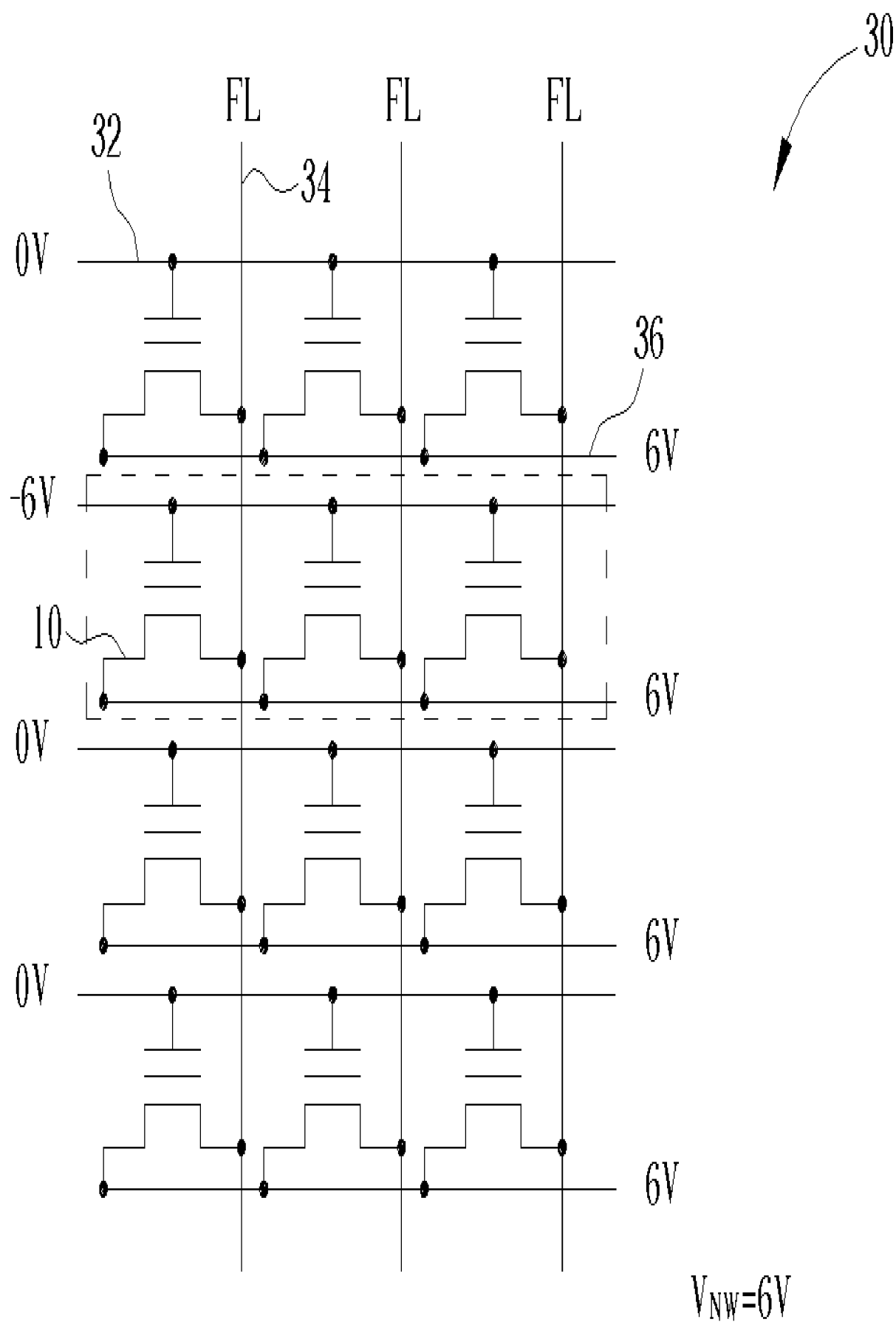


Fig. 8

| Voltage | Write | Read | Erase |
|----------|-------|------|-------|
| V_G | -2 | 0 | -6 |
| V_S | 0 | -1.8 | 6 |
| V_D | -6 | 0 | FL |
| V_{NW} | 0 | 0 | 6 |
| SL | 0 | -1.8 | 6 |
| WL1 | 1 | 2 | 0 |
| WL2 | -2 | 0 | -6 |
| WL3 | 1 | 2 | 0 |
| WL4 | 1 | 2 | 0 |
| BL1 | FL | FL | FL |
| BL2 | -6 | 0 | FL |
| BL3 | FL | FL | FL |

Fig. 9